

Errata: CS2000 Family Rev. B2, B3, and C1 Silicon

(See Tables 1 to 4 for specific devices affected by this errata and their referenced data sheets.)

- ◆ The Auto R-Mod feature does not operate reliably at the ratio boundaries defined in the data sheet. The actual boundaries can vary by a factor of 2 around the datasheet defined ratio trip points. As a result of this variance, the Auto R-Mod feature should not be used. The affected devices are shown in Tables 1 and 2 below.

Affected Device	Datasheet Revision
CS2000-CP	DS761PP1
CS2100-CP	DS840PP1
CS2300-CP	DS843PP1

Table 1. Affected -CP Devices

Affected Device	Datasheet Revision
CS2000-OTP	DS758PP1
CS2100-OTP	DS841PP1
CS2300-OTP	DS844PP1

Table 2. Affected -OTP Devices

- ◆ The following specification update applies to the devices in tables 3 and 4 below. Operation outside of this range may cause jitter levels higher than the datasheet maximum.

Crystal Frequency Fundamental Mode XTAL	f_{XTAL}	<i>RefClkDiv[1:0] = 10</i>	8	-	14	MHz
		<i>RefClkDiv[1:0] = 01</i>	16	-	28	MHz
		<i>RefClkDiv[1:0] = 00</i>	32	-	50	MHz
Reference Clock Input Frequency	f_{REF_CLK}	<i>RefClkDiv[1:0] = 10</i>	8	-	14	MHz
		<i>RefClkDiv[1:0] = 01</i>	16	-	28	MHz
		<i>RefClkDiv[1:0] = 00</i>	32	-	56	MHz

Affected Device	Datasheet Revision
CS2000-CP	DS761PP1
CS2200-CP	DS759PP1

Table 3. Affected -CP Devices

Affected Device	Datasheet Revision
CS2000-OTP	DS758PP1
CS2200-OTP	DS842PP1

Table 4. Affected -OTP Devices

Contacting Cirrus Logic Support

For all product questions and inquiries, contact a Cirrus Logic Sales Representative.
To find the one nearest you, go to <http://www.cirrus.com>